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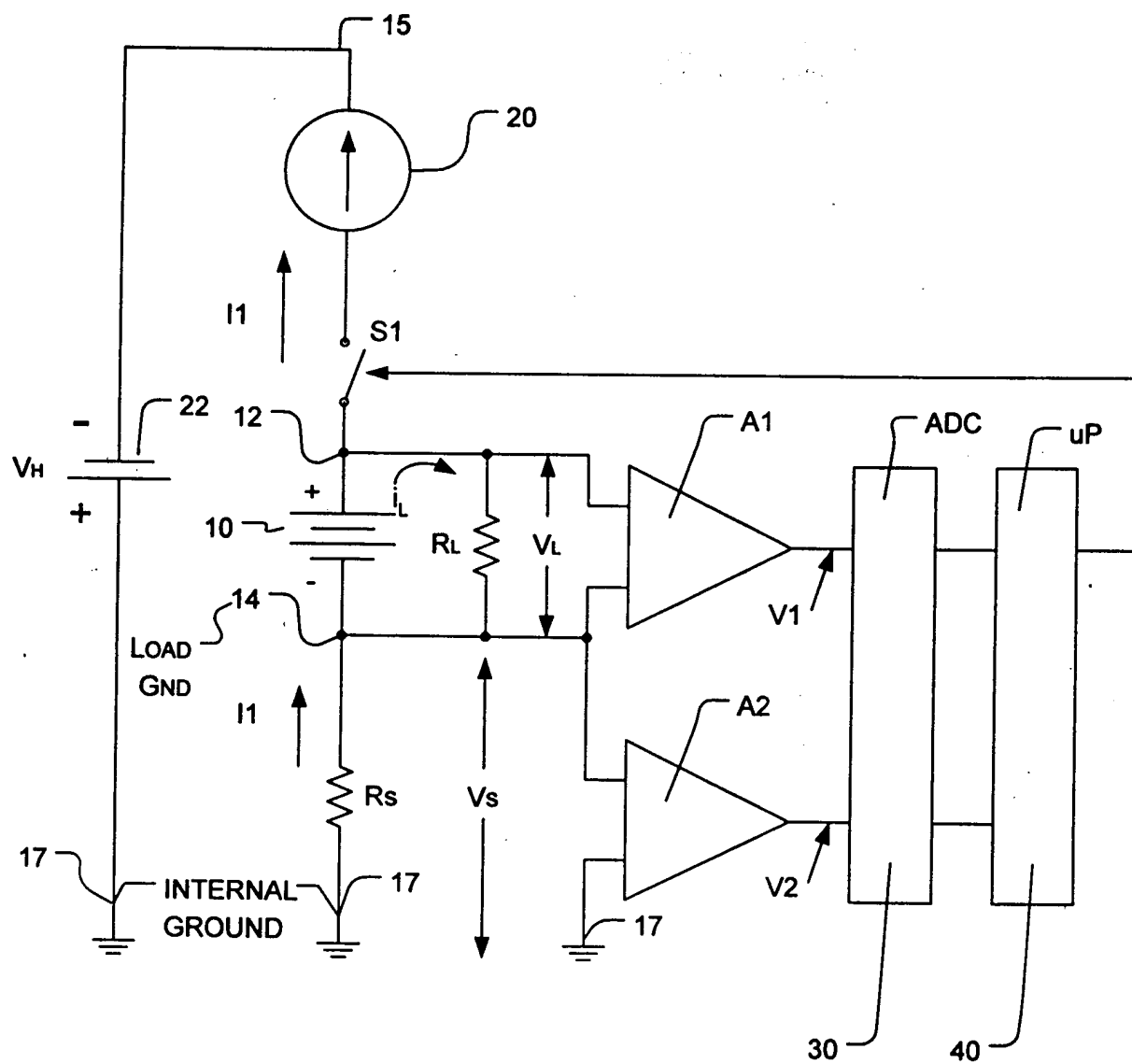


FIG 1

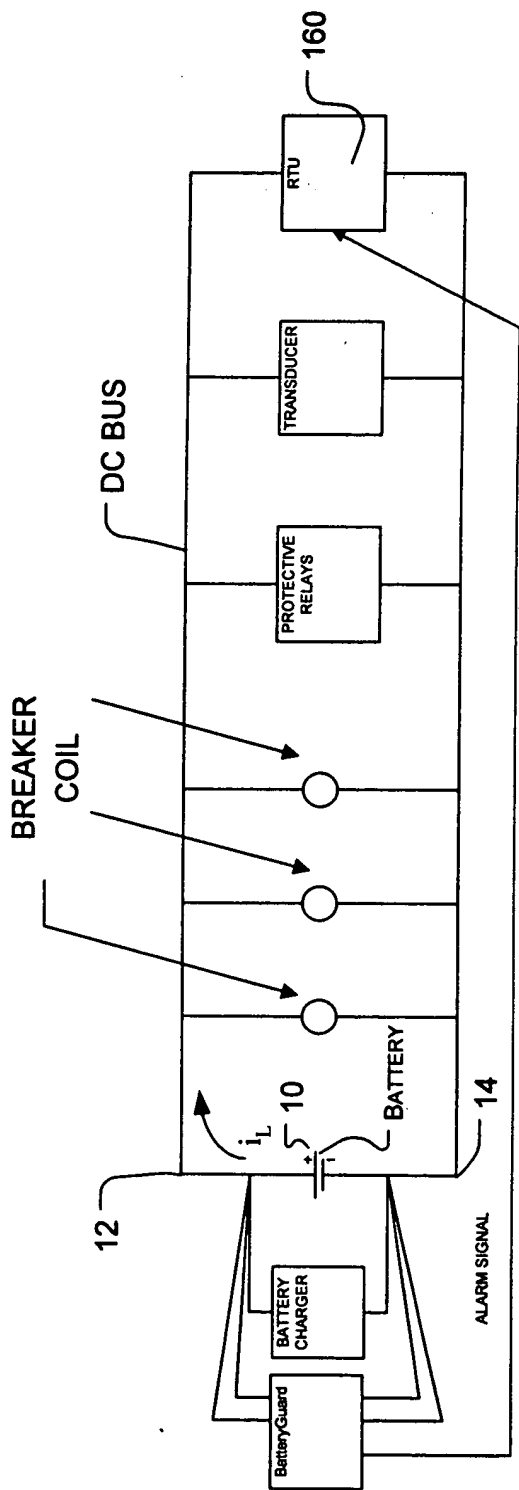


FIG 1A

The diagram shows a transimpedance amplifier circuit. A feedback loop is formed by a resistor  $R_L$  (10) and a capacitor  $C_F$  (22) connected between the inverting input and the output. The inverting input is also connected to a photodiode  $D_1$  (201) and a resistor  $R_2$  (203). A current source  $I$  is connected to the non-inverting input. The output of the amplifier is connected to an ADC (30a) and a MICRO PROCESSOR (40). The MICRO PROCESSOR is also connected to a CLOCK (210) and a feedback path to the inverting input. A second input stage, labeled A2 AMP (30b), is connected to the output of the first amplifier and provides a feedback signal to the inverting input. The circuit is powered by a DC source (15) and a ground (14).

**FIG 2**

205050" 059T800T

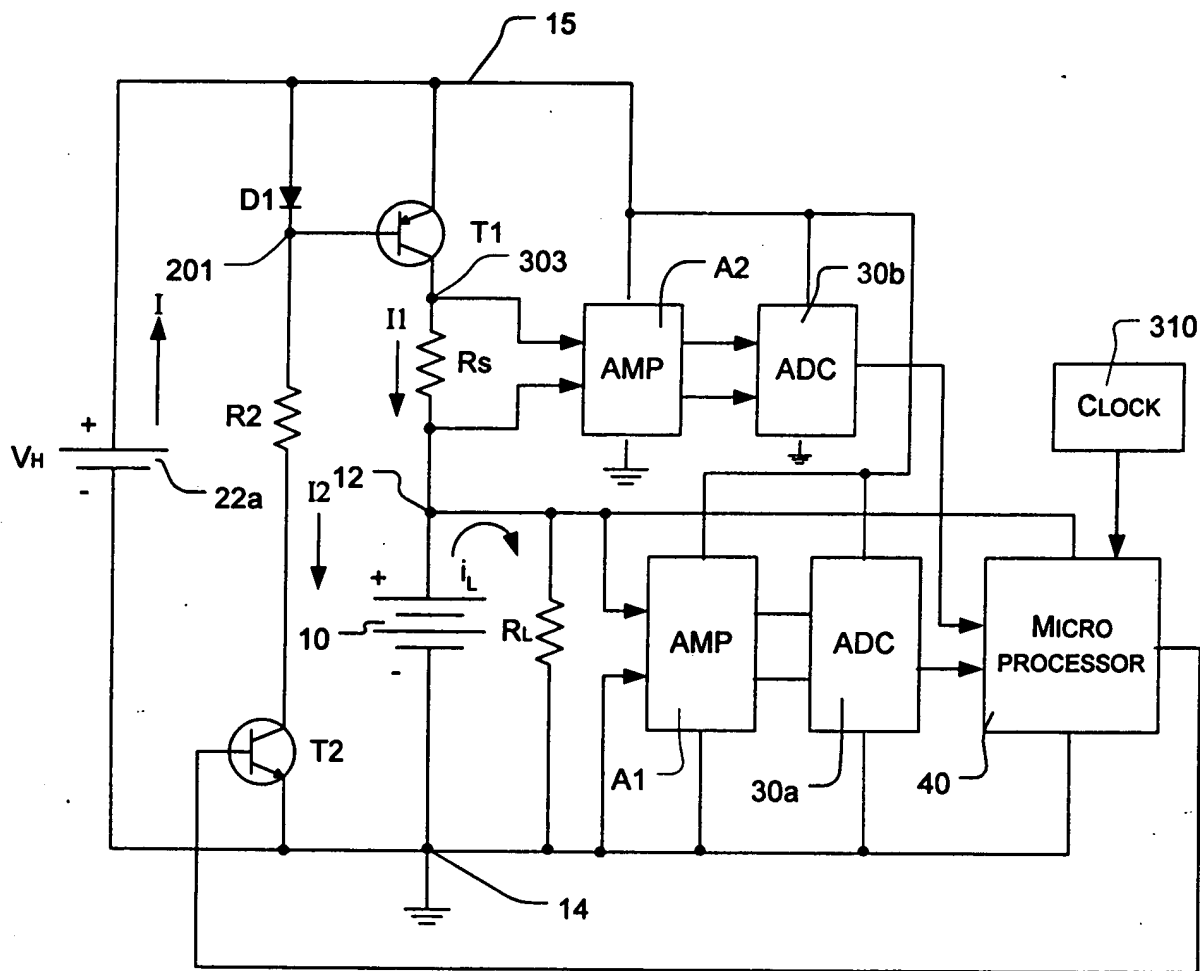


FIG 3

205050" 059T800T

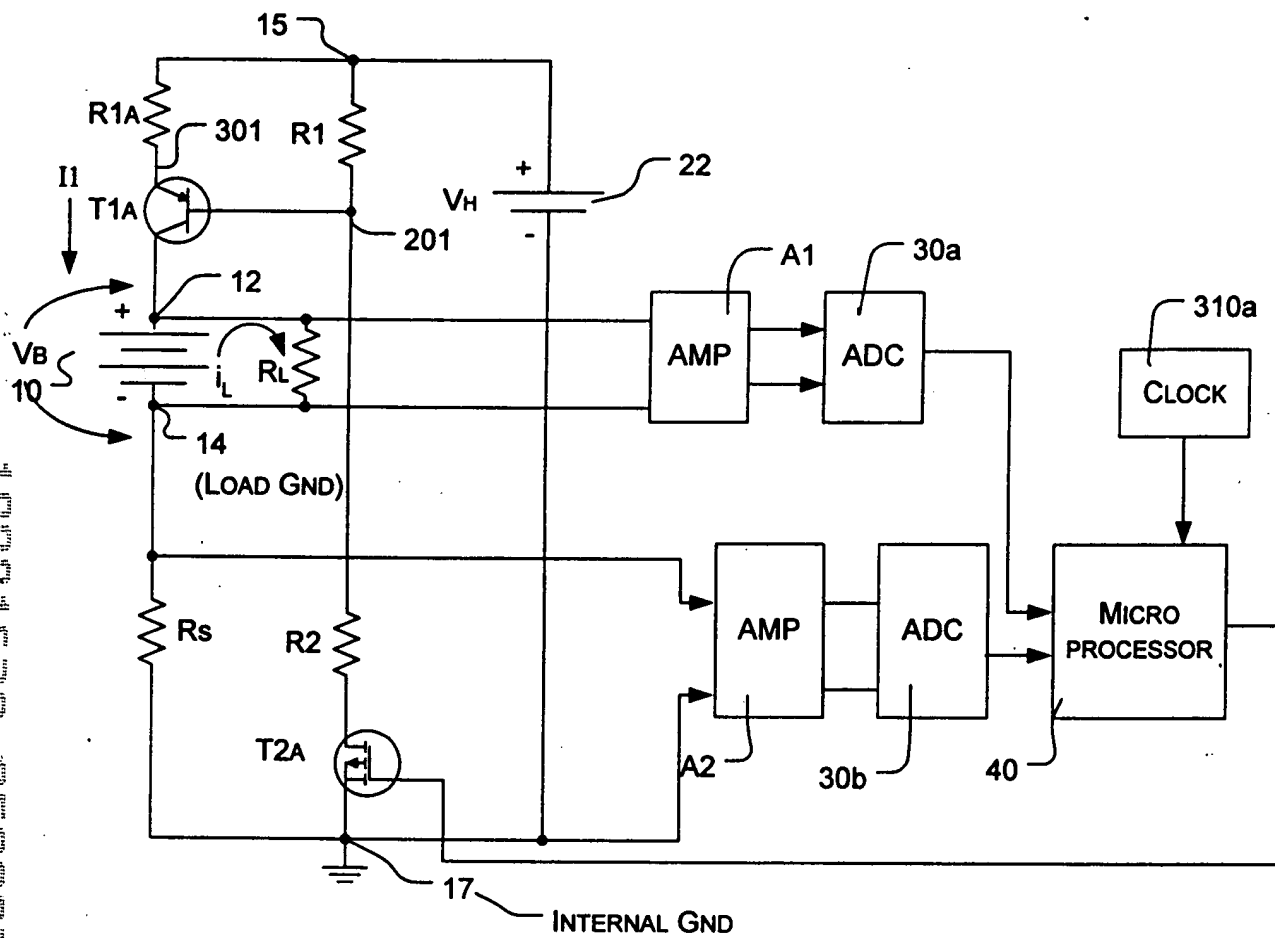


FIG 3A

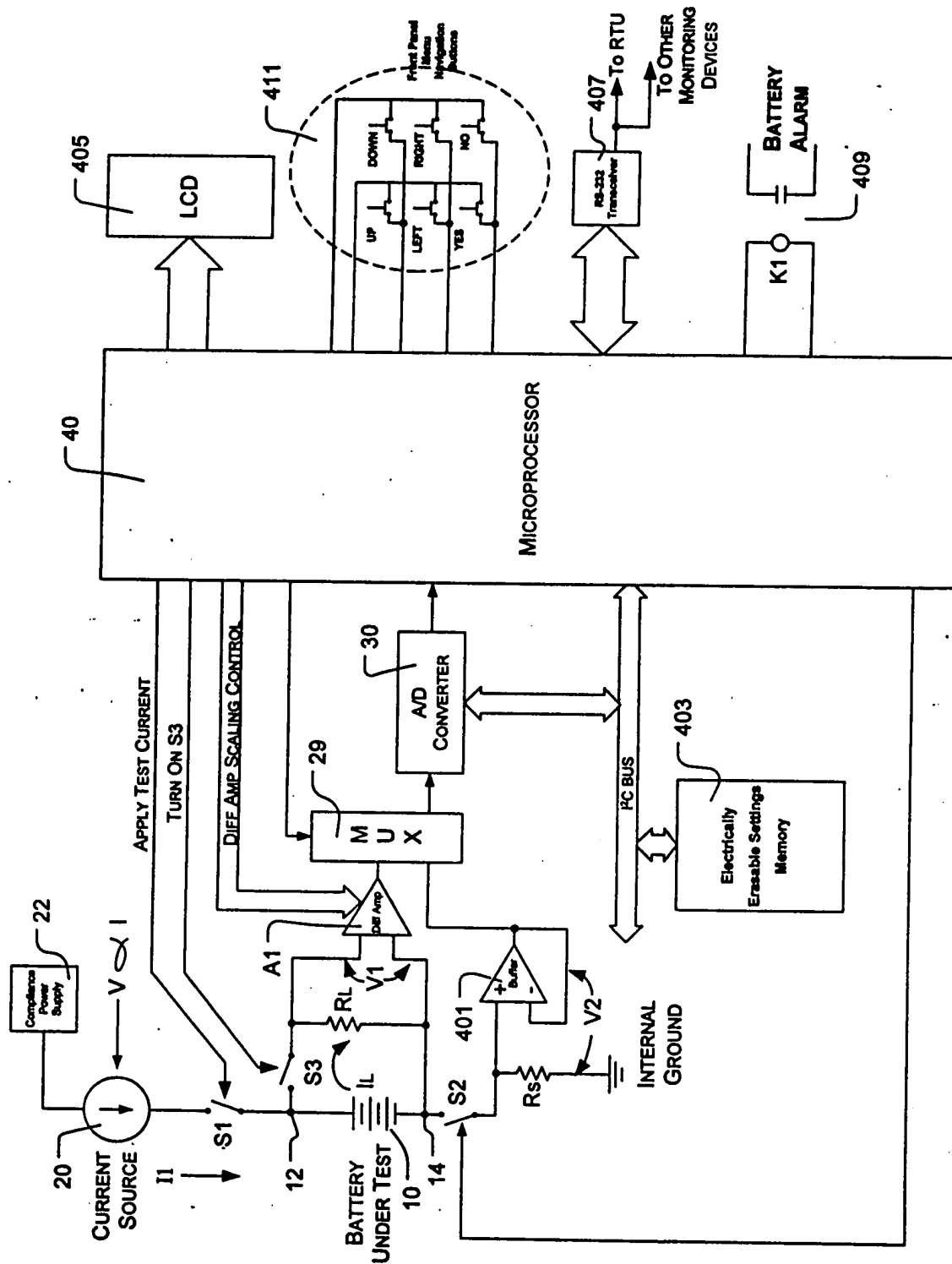


FIG 4

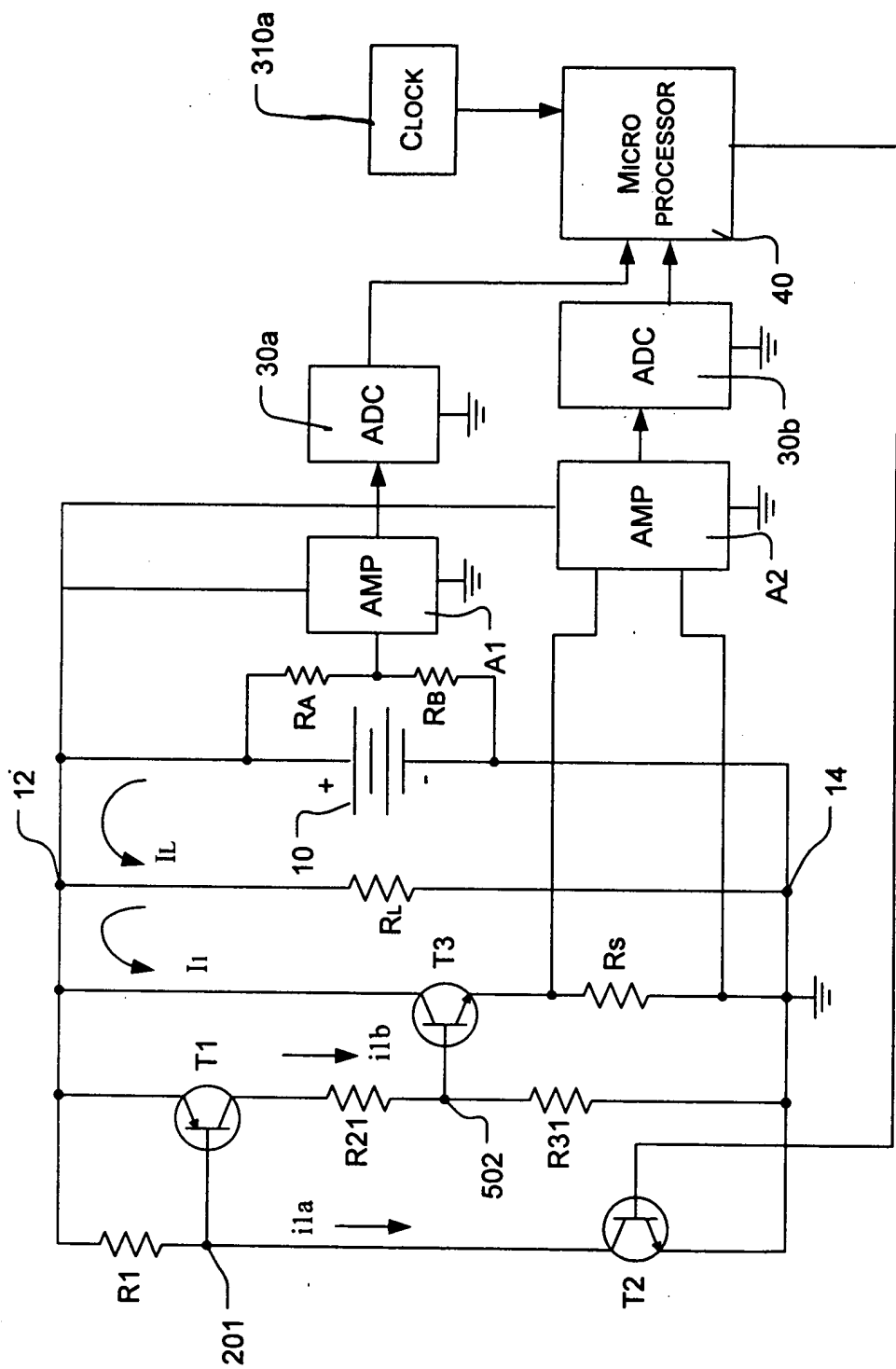


FIG 5